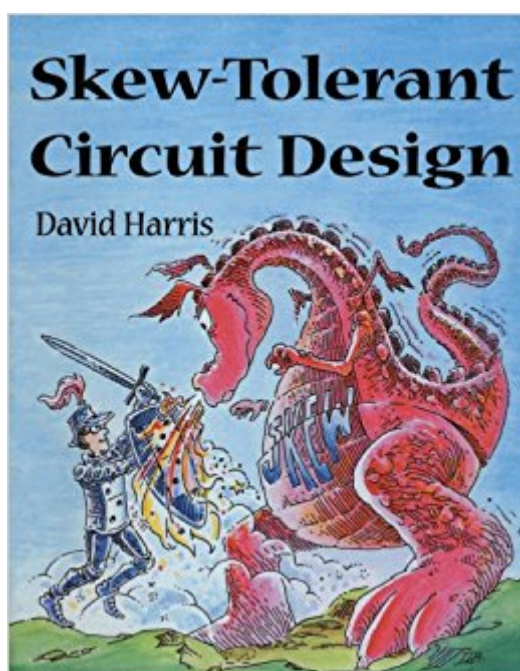


The book was found

Skew-Tolerant Circuit Design (The Morgan Kaufmann Series In Computer Architecture And Design)



Synopsis

As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues. Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial Provides incisive instruction and advice punctuated by humorous illustrations Includes exercises to test understanding of key concepts and solutions to selected exercises

Book Information

Series: The Morgan Kaufmann Series in Computer Architecture and Design

Paperback: 300 pages

Publisher: Morgan Kaufmann; 1 edition (June 5, 2000)

Language: English

ISBN-10: 155860636X

ISBN-13: 978-1558606364

Product Dimensions: 7.2 x 0.6 x 9.3 inches

Shipping Weight: 1.1 pounds (View shipping rates and policies)

Average Customer Review: 4.7 out of 5 stars 4 customer reviews

Best Sellers Rank: #2,192,664 in Books (See Top 100 in Books) #80 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI](#) #247 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Integrated](#) #369 in [Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Logic](#)

Customer Reviews

"Harris leads the way to more performance with a clear strategy for design. He shows how to combine logic and latching to do more logic in less time. In an era where less stuff means higher speed, everyone interested in high performance logic must understand these techniques or be left behind."-Ivan Sutherland, Vice President and Fellow, Sun Microsystems

"The author thoroughly explains important circuit design techniques including various types of latch design styles, clocking strategies, and methods of accounting for clock skew. That all of this is captured in one place is one of the great strengths of this book."-Emily J. Shriver, Alpha Development Group, Compaq Computer Corporation

"Harris leads the way to more performance with a clear strategy for design. He shows how to combine logic and latching to do more logic in less time. In an era where less stuff means higher speed, everyone interested in high performance logic must understand these techniques or be left behind."- Ivan Sutherland Vice President and Fellow, Sun Microsystems

"The author thoroughly explains important circuit design techniques including various types of latch design styles, clocking strategies, and methods of accounting for clock skew. That all of this is captured in one place is one of the great strengths of this book."- Emily J. Shriver Alpha Development Group, Compaq Computer Corporation

As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues.

Features:

- Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial
- Provides incisive instruction and advice punctuated by humorous illustrations
- Includes exercises to test understanding of key concepts and solutions to selected exercises

The quality of the product was good and i am happy with the purchase. I would like to purchase

some other books which are useful.

Excellent book on circuit design. The book is well constructed and explains things in easy to understand terms. Also, it gives real insight into circuit design from someone who has lots of experience. I highly, highly recommend!

I really liked this book. It is great for someone who is just beginning in circuit design like myself. I recommend it even if you are weak in the areas of device physics and VLSI.

It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on critical clock skew problems.

[Download to continue reading...](#)

Skew-Tolerant Circuit Design (The Morgan Kaufmann Series in Computer Architecture and Design)
Self-Checking and Fault-Tolerant Digital Design (The Morgan Kaufmann Series in Computer Architecture and Design)
Computer Organization and Design MIPS Edition, Fifth Edition: The Hardware/Software Interface (The Morgan Kaufmann Series in Computer Architecture and Design)
Computer Organization and Design, Fourth Edition: The Hardware/Software Interface (The Morgan Kaufmann Series in Computer Architecture and Design)
Foundations of Analog and Digital Electronic Circuits (The Morgan Kaufmann Series in Computer Architecture and Design)
Logical Effort: Designing Fast CMOS Circuits (The Morgan Kaufmann Series in Computer Architecture and Design)
See MIPS Run, Second Edition (The Morgan Kaufmann Series in Computer Architecture and Design)
Learning Processing, Second Edition: A Beginner's Guide to Programming Images, Animation, and Interaction (The Morgan Kaufmann Series in Computer Graphics)
Integrated circuit devices and components (Integrated-circuit technology, analog and logic circuit design, memory and display devices)
Computer Networks, Fifth Edition: A Systems Approach (The Morgan Kaufmann Series in Networking)
Computer Networks: A Systems Approach (The Morgan Kaufmann Series in Networking)
Winter Circuit (Show Circuit Series -- Book 2) (The Show Circuit)
VLSI Test Principles and Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon)
Game Feel: A Game Designer's Guide to Virtual Sensation (Morgan Kaufmann Game Design Books)
Data Mining: Practical Machine Learning Tools and Techniques (Morgan Kaufmann Series in Data Management Systems)
Data Mining: Concepts and Techniques, Third Edition (The Morgan Kaufmann Series in Data Management Systems)
Data Mining, Fourth Edition: Practical Machine Learning Tools and Techniques (Morgan Kaufmann Series in Data Management Systems)

Data Mining: Practical Machine Learning Tools and Techniques, Third Edition (Morgan Kaufmann Series in Data Management Systems) Data Mining: Practical Machine Learning Tools and Techniques, Second Edition (Morgan Kaufmann Series in Data Management Systems) Blondie24: Playing at the Edge of AI (The Morgan Kaufmann Series in Artificial Intelligence)

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)